IN THE SPECIFICATION

Please replace the paragraph beginning on page 10, line 20 with the following paragraph:

Referring to FIG. 3, a block diagram of the circuit 112 of FIG. 2 is shown. The circuit 112 and the circuit 114 may be implemented similarly. As such, only the circuit 112 is described for brevity. The circuit 112 may comprise a circuit 140 and a circuit 142. The circuit 140 may be configured to generate the serial streams A_SER1-A_SERM in response to the source data streams ~~A_SRC1-A_SRCM~~ <u>A_SRC1-A_SRCN</u>. The number of source data streams N may be larger than or equal to the number of serial streams M. In one example, N may be an integer multiple of M. The circuit 142 may be configured to recover a number of source data streams from one or more serial streams. For example, the circuit 142 may recover the source data streams B_SRC1-B_SCRN from the serial streams B_SER1-B_SERM.

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